ISOLATED DC-DC CONVERTER
CHASSIS MOUNT
CFB750-300SXX-CMFD SERIES
APPLICATION NOTE

Approved By:

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Design Quality Department	Benny	JoJo	

Contents

CFB750-300S CMFD Series
Application Note V11

1. Introduction

The CFB750-300SXX-CMFD series of DC-DC converters offers 750 watts of output power @ single output voltages of 12, 15, 24, 28, 36, 48VDC with industry standard full-brick module inside. It has a wide (2:1) input voltage range of 200 to 425VDC (300VDC nominal) and 3000VAC reinforced isolation.

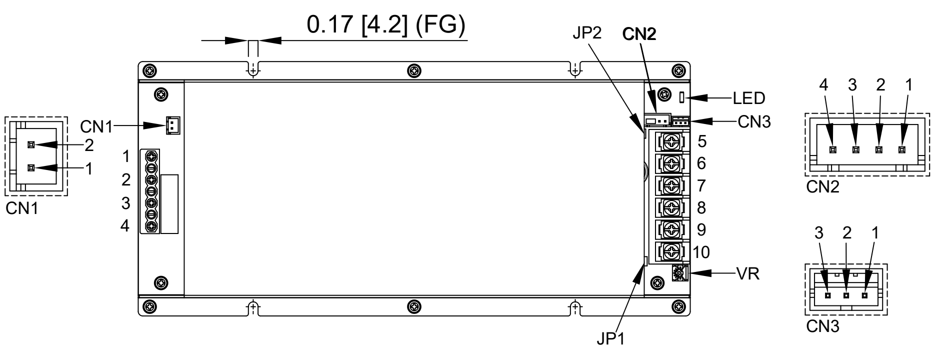
Compliant with EN55032, EN45545. High efficiency up to 90%, allowing case operating temperature range of -40℃ to 80℃. An optional heat sink is available to extend the full power range of the unit. Very low no load power consumption (10mA), an ideal solution for energy critical systems.

The standard control functions include remote on/off (positive or negative) and +10%, -40% adjustable output voltage (**Section 6.7/6.8**).

Fully protected against input UVLO (under voltage lock out), output over-current, output over-voltage and over-temperature and continuous short circuit conditions.

CFB750-300SXX-CMFD series is highly suitable for distributed power architectures, telecommunications, servers, base station, battery operated equipment, and industrial applications.

2. Pin Function Description



No	PIN Function	Description	Reference	No	CN2	Description	Reference
1	Case	Connected to Base Plate		1	-Sense	Negative Output Remote Sense	**Section 6.6**
2	Rem	External Remote On/Off Control	**Section 6.5**	2	+Sense	Positive Output Remote Sense	**Section 6.6**
3	-Vi	Negative Supply Input	**Section 7.1**	3	Trim	External Output Voltage Adjustment	**Section 6.7/6.8**
4	+Vi	Positive Supply Input	**Section 7.1**	4	Rt		
5-7	-Vo	Negative Power Output	**Section 7.2/7.3**				
8-10	+Vo	Positive Power Output	**Section 7.2/7.3**		JP1	Short +S & +Vo	**Section 6.6/6.8**
--	--	Clear Mounting Insert (FG)			JP2	Short -S & -Vo	**Section 6.6/6.8**

No	CN1	Description	Reference	No	CN3	Description	Reference
1	+Remote On/Off	External Remote On/Off Control	**Section 6.5**	1	AUX	Auxiliary Power for Output	**Section 6.10**
2	-Remote On/Off	Connected to Negative Supply Input	**Section 6.5**	2	IOG	Inverter Operation Good Signal	**Section 6.9**
				3	PC	Parallel Operation Control	**Section 8.2**

Note: Base plate can be connected to FG through Ø4.2 mounting insert. Recommended torque 9.6~12.8Kgf-cm.

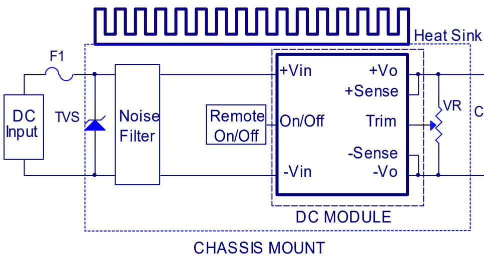
3. Terminal Block

Input and Output Terminal Block

Terminal Type	Screw Torque Value (Kgf-cm)	Suitable Electric Wire (AWG)	Current Rating (max.)
166-04P5 or Equivalent	5	12-18	12A
DT-49-B01W-xx or Equivalent	10	12-22	25A

4. Connection for Standard Use

The connection for standard use is shown below. An external output capacitors (C1) is recommended to reduce output ripple and noise, output capacitor recommended 1uF ceramic capacitor for all models.



Symbol	Component	Reference
F1, TVS	Input fuse, TVS	**Section 10.1**
Noise Filter	Internal input noise filter	**Section 10.2**
Remote On/Off	External Remote On/Off control	**Section 6.5**
Trim	Internal output voltage adjustment By variable resistor	**Section 6.7/6.8**
Heat sink	External heat sink	**Section 9.4**
+Sense/-Sense	Positive / Negative Output Sense	**Section 6.6**

5. Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown below. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate:

- Efficiency
- Load regulation and line regulation

The value of efficiency is defined as:

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where:

V_o is output voltage,
I_o is output current,
V_{in} is input voltage,
I_{in} is input current.

The value of load regulation is defined as:

$$Load\ reg. = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where:

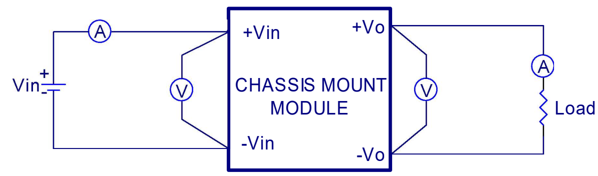
V_{FL} is the output voltage at full load
V_{NL} is the output voltage at no load

The value of line regulation is defined as:

$$Line\ reg. = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where:

V_{HL} is the output voltage of maximum input voltage at full load.
V_{LL} is the output voltage of minimum input voltage at full load.

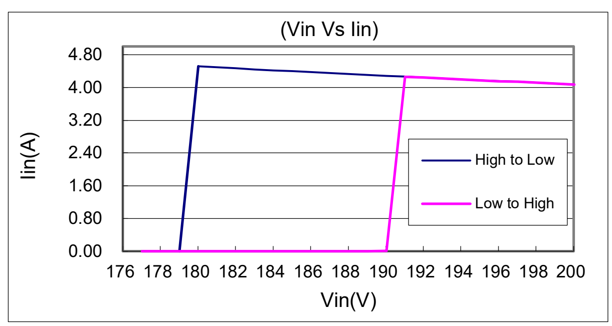


CFB750-300SXX-CMFD Series Test Setup

6. Features and Functions

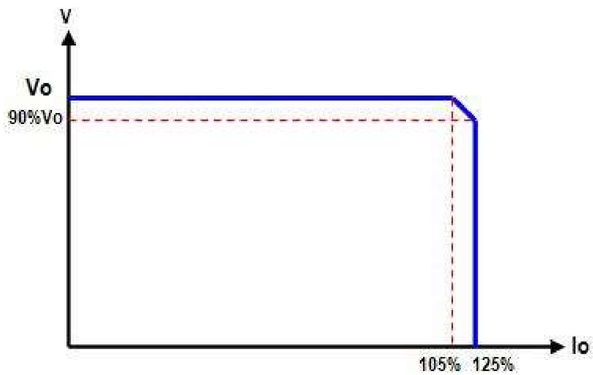
6.1 UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the CFB750-300SXX-CMFD series unit. The unit will shut down when the input voltage drops below a threshold, and the unit will operate when the input voltage goes above the upper threshold.



6.2 Over Current Protection

The converter is protected against over current or short circuit conditions. At the instance of current-limit inception, the module enters a constant current mode of operation. While the fault condition exists, the module will remain in this constant current mode, and can remain in this mode until the fault is cleared. The unit operates normally once the output current is reduced back into its specified range.



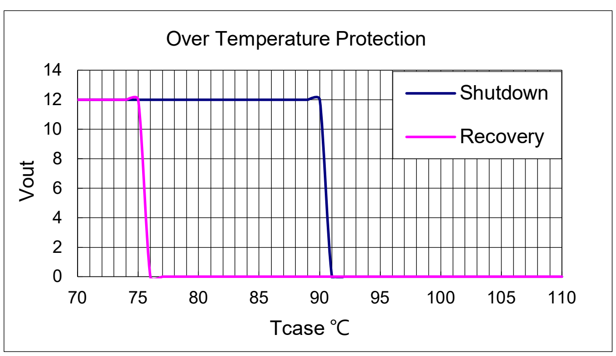
6.3 Output Over Voltage Protection

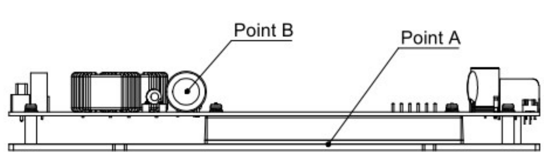
The output over voltage protection consists of circuitry that internally limits the output voltage. If more accurate output over voltage protection is required then an external circuit can be used via the remote **on/off** pin.

Note: Please note that device inside the power supply might fail when voltage more than rate output voltage is applied to output pin. This could happen when the customer tests the over voltage protection of unit.

6.4 Over Temperature Protection

These modules have an over temperature protection circuit to safeguard against thermal damage. Shutdown occurs with the maximum case reference temperature is exceeded. The module will restart when the case temperature falls below over temperature recovery threshold. Please measured at point A (measuring point A refer to the following figure).





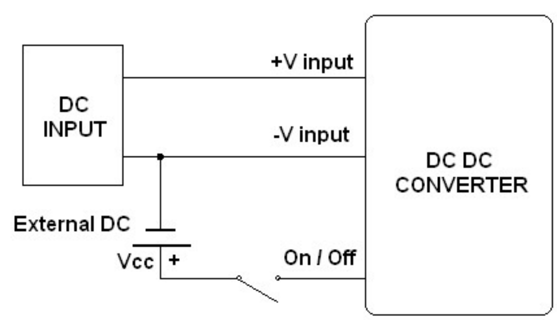
6.5 Remote On/Off

The CFB750-300SXX-CMFD series allows the user to switch the module on and off electronically with the remote **on/off** feature. All models are available in "positive logic" and "negative logic" (optional) versions. The converter turns on if the remote **on/off** pin is high (>3.5Vdc to 12Vdc). Setting the pin low (0 to<1.2Vdc or open circuit) will turn the converter off. The signal level of the remote **on/off** input is defined with respect to ground. If leave the remote **on/off** pin open, the converter will be off.

Models with part number suffix "N" are the "negative logic" remote **on/off** version. The unit turns off if the remote **on/off** pin is high (>3.5Vdc to 12Vdc). The converter turns on if the **on/off** pin input is low (0 to<1.2Vdc or open circuit). Note that the converter is on by default.

Logic State (CN1 Pin 3)	Negative Logic	Positive Logic
Logic Low – 0 to 1.2Vdc or **Open circuit**	Module on	Module off
Logic High – 3.5 to 12Vdc	Module off	Module on

When **on/off** is at high level, a current of 10mA max will sink in. Avoid the reverse polarity input voltage. It may break the power supply. Connection examples see below.



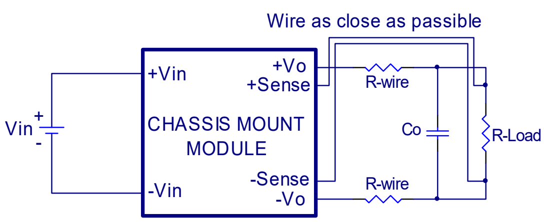
Remote On/Off Connection Example

6.6 Output Remote Sensing

The CFB750-300SXX-CMFD series converter has the capability to remotely sense both lines of its output. This feature moves the effective output voltage regulation point from the output of the unit to the point of connection of the remote sense pins. This feature automatically adjusts the real output voltage of the CFB750-300SXX-CMFD series in order to compensate for voltage drops in distribution and maintain a regulated voltage at the point of load. The remote-sense voltage range is:

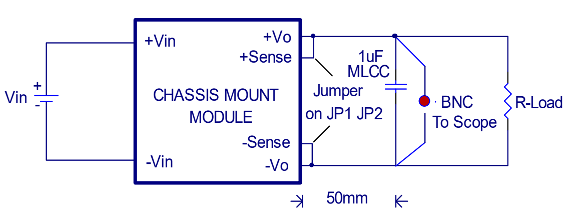
$[(+V_{out}) - (-V_{out})] - [(+Sense) - (-Sense)] \leqq 10\%$ of $V_{o\_nominal}$

When remote sensing is used, please remove the jumper of JP1&JP2. When remote sense is in use, the sense should be connected by twisted-pair wire or shield wire. If the sensing patterns short, heavy current flows and the pattern may be damaged. Output voltage might become unstable because of impedance of wiring and load condition when length of wire is exceeding 400mm. This is shown in the schematic below.



Output Remote Sensing

When the CMFD module are shipped from a factory, they come with a dedicated jumper being mounted on JP1&JP2. If the remote sense feature is not to be used, the sense pins should be connected locally. The +Sense pin should be connected to the +Vout pin at the module and the -Sense pin should be connected to the -Vout pin at the module. Wire between +Sense and +Vout and between -Sense and –Vout as short as possible. Loop wiring should be avoided. The converter might become unstable by noise coming from poor wiring. This is shown in the schematic below.

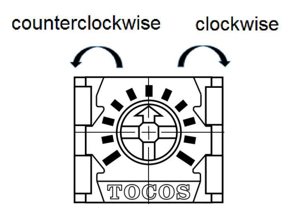


Note: Although the output voltage can be varied (increased or decreased) by both remote sense and trim, the maximum variation for the output voltage is the larger of the two values not the sum of the values. The output power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. Using remote sense and trim can cause the output voltage to increase and consequently increase the power output of the module if output current remains unchanged. Always ensure that the output power of the module remains at or below the maximum rated power. Also be aware that if $V_{o.set}$ is below nominal value, $P_{out.max.}$ will also decrease accordingly because $I_{o.max.}$ is an absolute limit. Thus, $P_{out.max.} = V_{o.set} \times I_{o.max.}$ is also an absolute limit.

6.7 Output Voltage Adjustment

Output voltage can be adjusted by internal variable resistor (adjustment range shown in the table below). Turning internal variable resistor clockwise reduces the output voltage and counterclockwise increases the output voltage.

Model Number	Trim Up Range	Trim Down Range
CFB750-300S12(N)-CMFD	+10%	-40%
CFB750-300S15(N)-CMFD	+10%	-40%
CFB750-300S24(N)-CMFD	+10%	-30%
CFB750-300S28(N)-CMFD	+10%	-30%
CFB750-300S36(N)-CMFD	+10%	-20%
CFB750-300S48(N)-CMFD	+10%	-15%

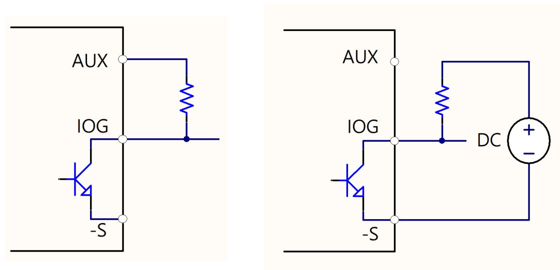


6.8 External Output Voltage Trim Range

Output may be externally trimmed, need to remove jumpers on JP1 and JP2 and the jumper on Pin 3&Pin4 of CN2 to reach the range of -40%~+10% output voltage. For details, please refer to CFB750-300SXX series application note.

6.9 IOG Signal

Normal and abnormal operation of the converter can be monitored by using the I.O.G signal. Output of this signal monitor is located at the secondary side and is open collector output, you can use the signal by the internal aux power supply or the external DC supply as the following figures. the ground reference is the -sense.



By internal AUX　　By external DC supply

This signal is low when the converter is normally operating and high when the converter is disabled or when the converter is abnormally operating.

6.10 Auxiliary Power for Output Signal

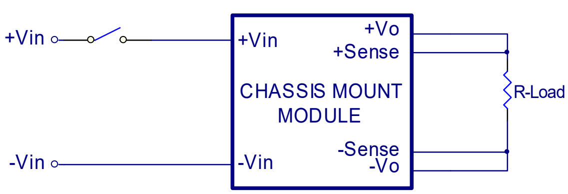
The auxiliary power supply output is within 7-13V with maximum current of 20 mA. Ground reference is the -sense Pin.

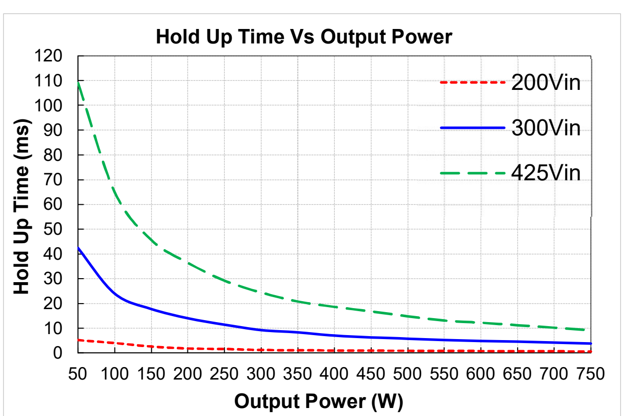
7. Input/Output Considerations

7.1 Hold Up Time

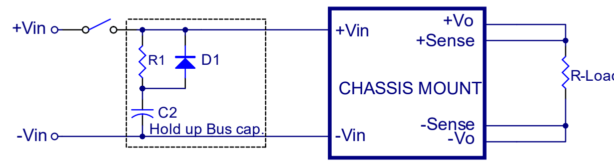
Hold up time is defined as the duration of time that DC/DC converter output will remain active following a loss of input power.

The test condition and test curve refer to following figures.





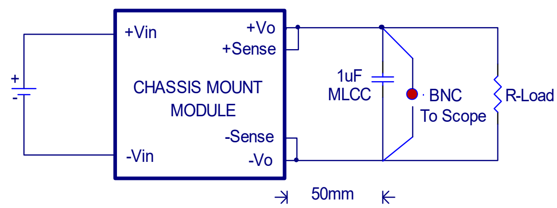
The external circuit about extend hold up time refer to following figure.



D1:600V/10A
R1:100Ω/10W

C2	200Vin	300Vin	425Vin
Hold up time for 10ms	3300uF	330uF	68uF
Hold up time for 30ms	8250uF	1080uF	330uF

7.2 Output Ripple and Noise

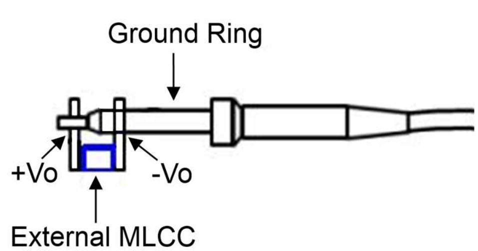


Output Ripple and Noise

Output ripple and noise measured with 1uF ceramic capacitors across output. A 20 MHz bandwidth oscilloscope is normally used for the measurement.

The conventional ground clip on an oscilloscope probe should never be used in this kind of measurement. This clip, when placed in a field of radiated high frequency energy, acts as an antenna or inductive pickup loop, creating an extraneous voltage that is not part of the output noise of the converter.

Another method is shown in below, in case of coaxial-cable/BNC is not available. The noise pickup is eliminated by pressing scope probe ground ring directly against the -Vout terminal while the tip contacts the +Vout terminal. This makes the shortest possible connection across the output terminals.



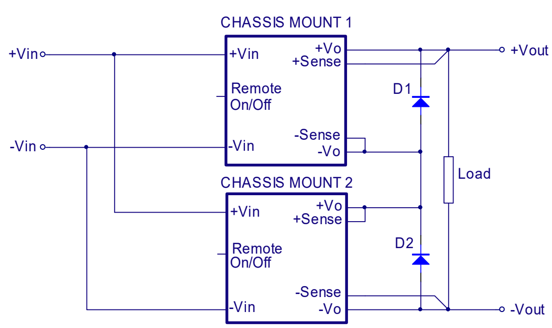
7.3 Output Capacitance

The CFB750-300SXX-CMFD series converters provide unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load (<100mm). PCB design emphasizes low resistance and inductance tracks in consideration of high current applications. Output capacitors with their associated ESR values have an impact on loop stability and bandwidth. Cincon's converters are designed to work with load capacitance to see technical specifications.

8. Series and Parallel Operation

8.1 Series Operation

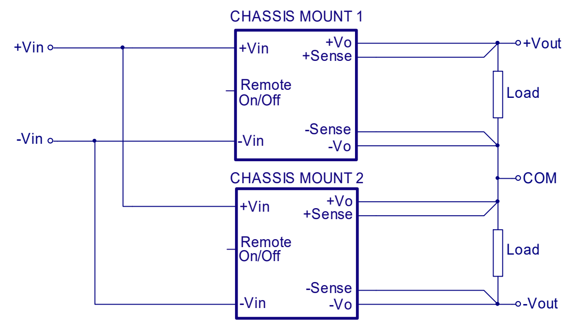
Series operation is possible by connecting the outputs two or more units. Connection is shown in below. The output current in series connection should be lower than the lowest rate current in each power module.



Simple Series Operation Connect Circuit

Note:
Recommend Schottky diode (D1, D2) be connected across the output of each series connected converter, so that if one converter shuts down for any reason, then the output stage won't be thermally overstressed. Without this external diode, the output stage of the shut-down converter could carry the load current provided by the other series converters, with its MOSFETs conducting through the body diodes. The MOSFETs could then be overstressed and fail. The external diode should be capable of handling the full load current for as long as the application is expected to run with any unit shut down.

Series for ±output operation is possible by connecting the outputs two units, as shown in the schematic below.



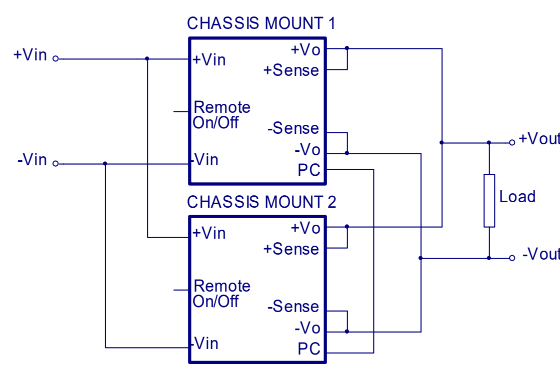
Simple ±Output Operation Connect Circuit

8.2 Parallel/Redundant Operation

The CFB750-300SXX-CMFD are also designed for parallel operation. When paralleled, the load current can be equally shared between the modules by connecting the PC pins together.

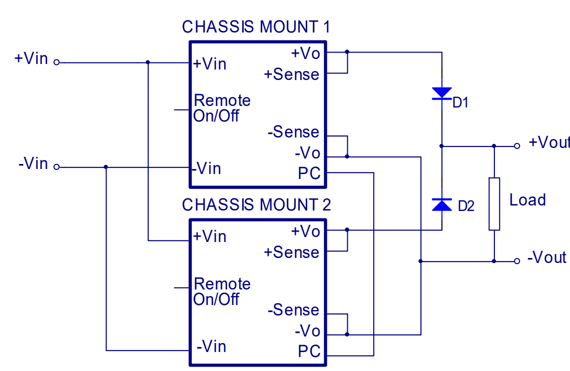
There are two different parallel operations for CFB750-300SXX-CMFD, one is parallel operation when load can't be supplied by only one power unit; the other is the N+1 redundant operation which is high reliable for load of N units by using N+1 units.

(a) parallel operation



Simple Parallel Operation Connect Circuit

(b) N+1 redundant connection



Simple Redundant Operation Connect Circuit

9. Thermal Design

9.1 Operating Temperature Range

The CFB750-300SXX-CMFD series converters can be operated within a wide case temperature range of -40℃ to 80℃. Consideration must be given to the derating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn from chassis mount models is influenced by usual factors, such as:

- Input voltage range
- Output load current
- Forced air or natural convection

9.2 Convection Requirements for Cooling

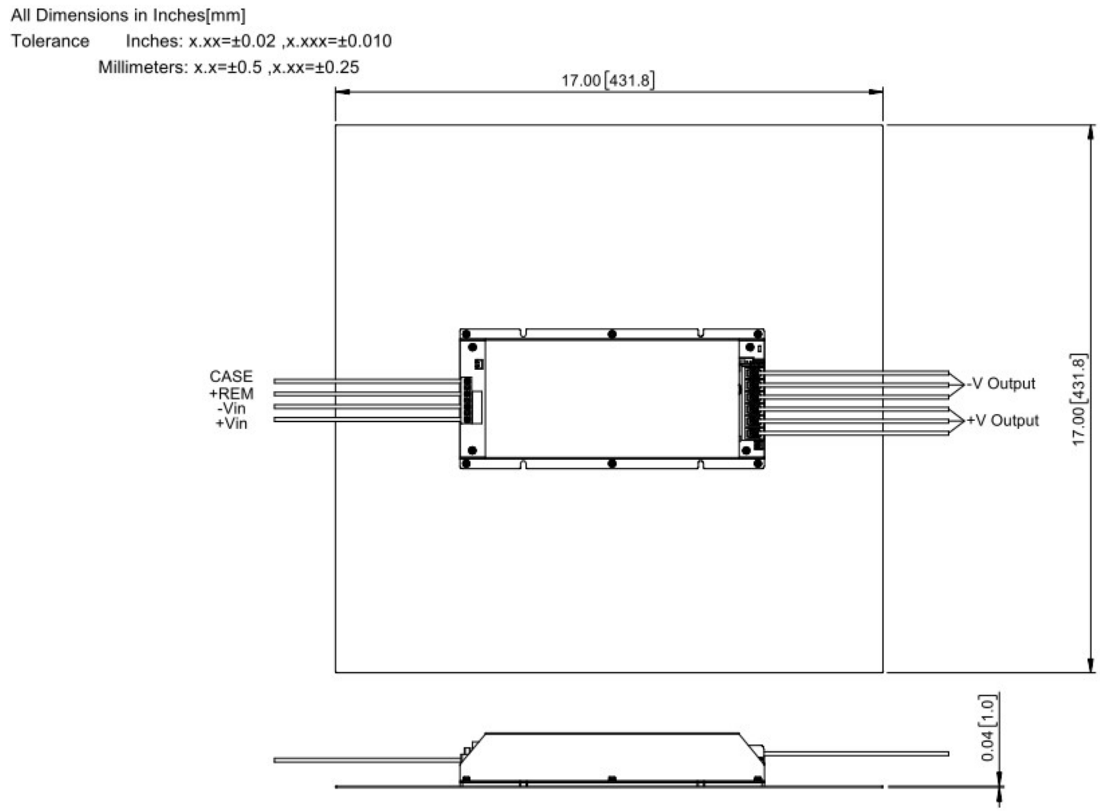
To predict the approximate cooling needed for the chassis mount module, refer to the power derating curves in **section 9.4**. These derating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the module's aluminum plate (point A) and aluminum capacitor (point B) temperature should be monitored to ensure it does not exceed 80℃ (measuring point A and measuring point B refer to the **section 6.4**).

9.3 Thermal Considerations

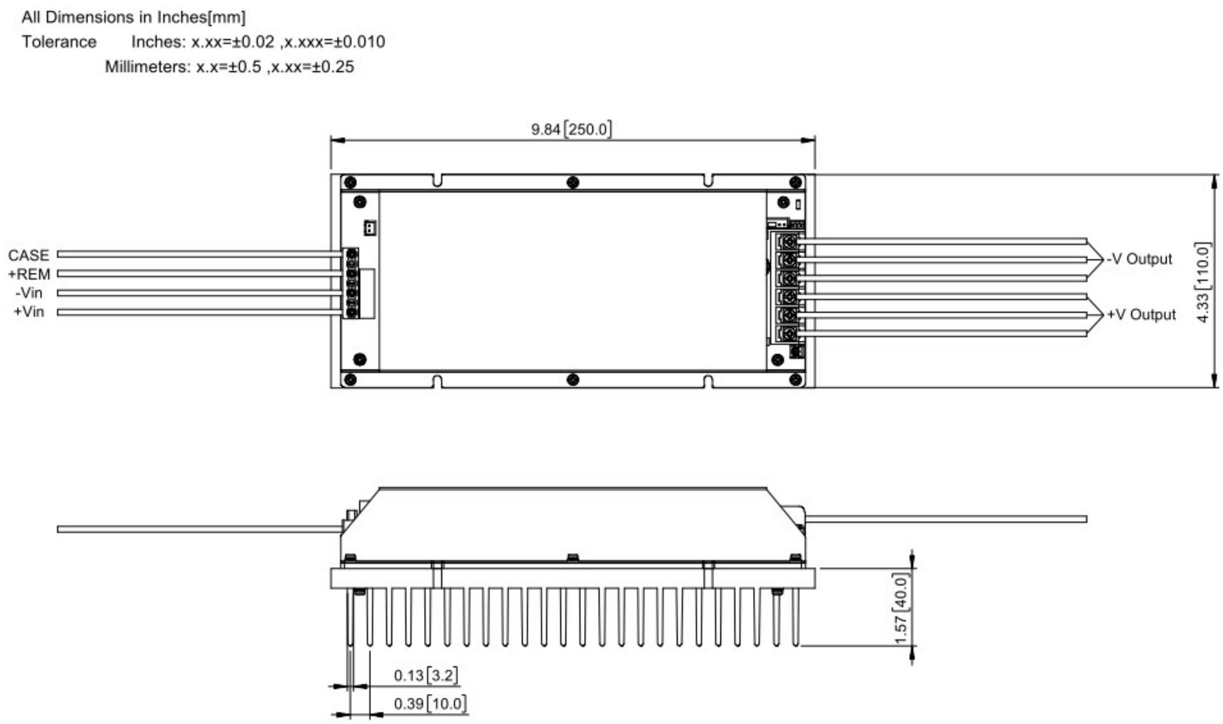
The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The example is presented in **section 9.4**. The power output of the module should not be allowed to exceed rated power (V_{o_set} x $I_{o\_max.}$).

9.4 Power Derating

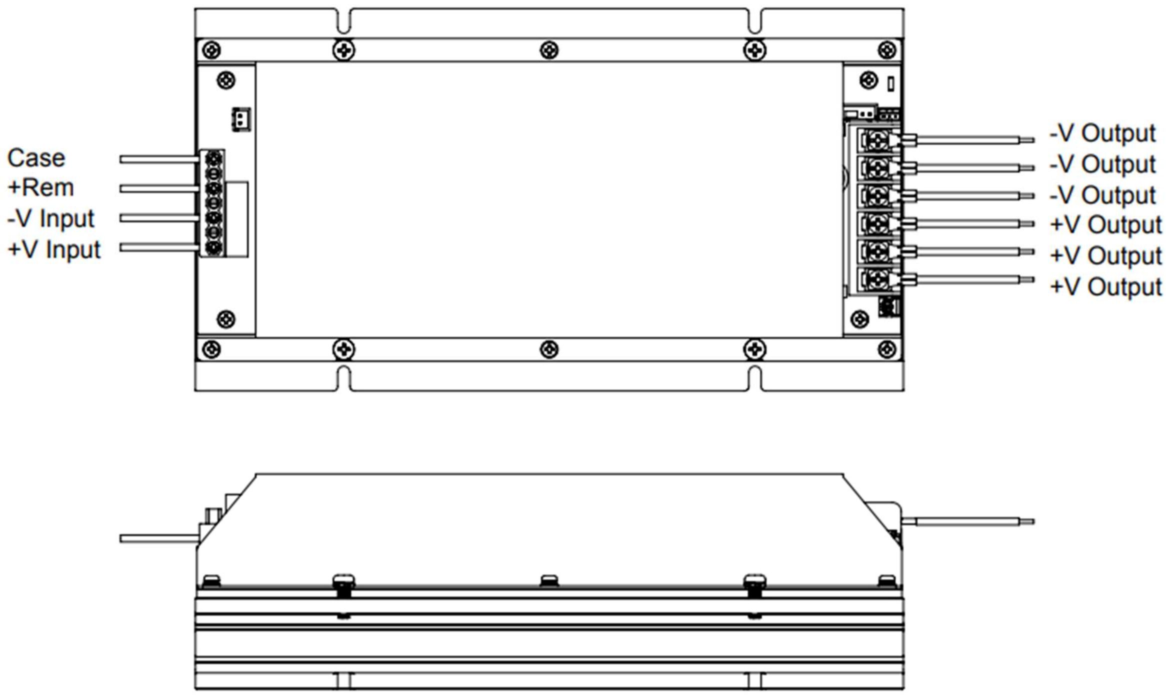
The operating case temperature range of CFB750-300SXX-CMFD series is -40℃ to +80℃. When operating the CFB750-300SXX-CMFD series, proper derating or cooling is needed. The point A and point B maximum temperature under any operating condition should not exceed 80℃ (point A and point B refer to the **section 6.4**). The following curve is the de-rating curve of CFB750-300SXX-CMFD series with heat sink.



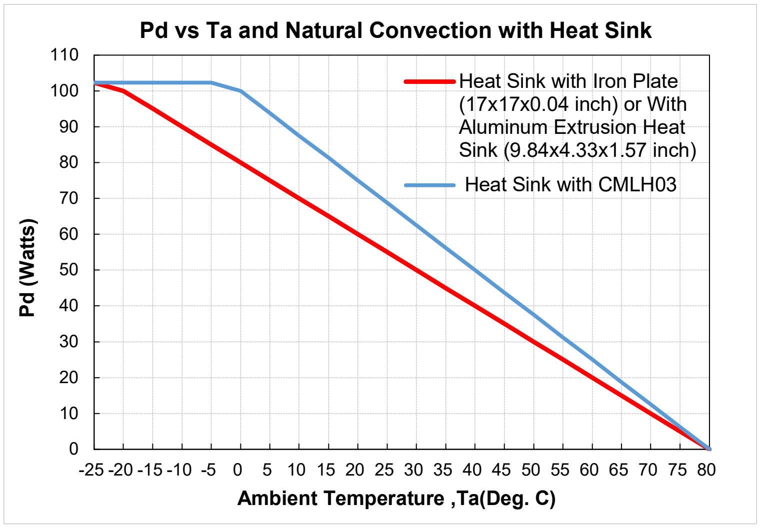
Figures 1. CFB750-300SXX-CMFD with Heat Sink Iron Plate (17x17x0.04 inch)



Figures 2. CFB750-300SXX-CMFD with Aluminum Extrusion Heat Sink (9.84x4.33x1.57 inch)



Figures 3. CFB750-300SXX-CMFD with Heat Sink CMLH03



AIR FLOW RATE Natural Convection 20ft./min. (0.1m/s)	TYPICAL R_{ca}
Heat Sink with Iron Plate (17x17x0.04 inch)	1.0 ℃/W
Heat Sink With Aluminum Extrusion (9.84x4.33x1.57 inch)	
Heat Sink with CMLH03	0.8 ℃/W

Example (with heat sink):
How to make a CFB750-300S48-CMFD operating at nominal line voltage, an output current of 10A, and a maximum ambient temperature of 25℃?

Solution:

Given:

V_{in}=300Vdc, V_o=48Vdc, I_o=10A

Determine power dissipation (P_d):

$P_d=P_i-P_o=P_o(1-\eta)/\eta$

P_d=48.0×10×(1-0.90)/0.90=53.3Watts

Determine airflow:

Given: P_d=53.3W and T_a=25℃

Check above power de-rating curve:

Heat sink with 17x17x0.04 inch

Verify:

Maximum temperature rise is $\triangle T=P_d \times R_{ca}$ =53.3×1=53.3℃

Maximum case temperature is $T_c=Ta+\triangle T$=78.3℃<80℃

Where:

The R_{ca} is thermal resistance from case to ambient environment

Ta is ambient temperature and T_c is case temperature

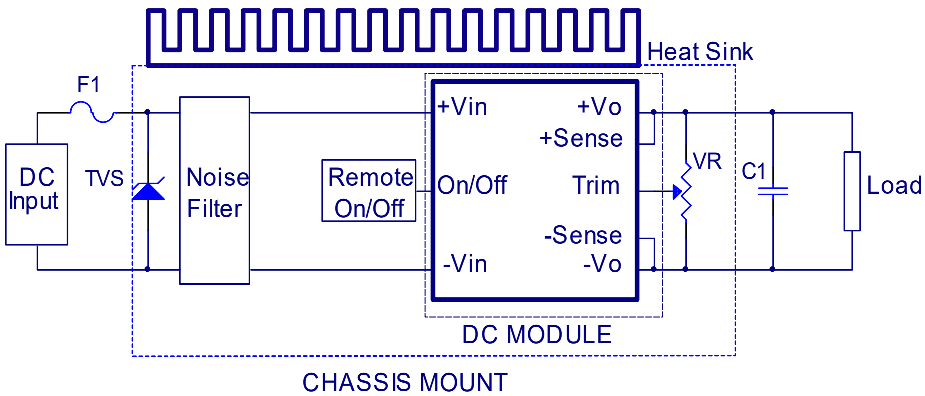
9.5 Heat Sink

Heat sinks assembly **refer to Datasheet-Thermal**

10. Safety & EMC

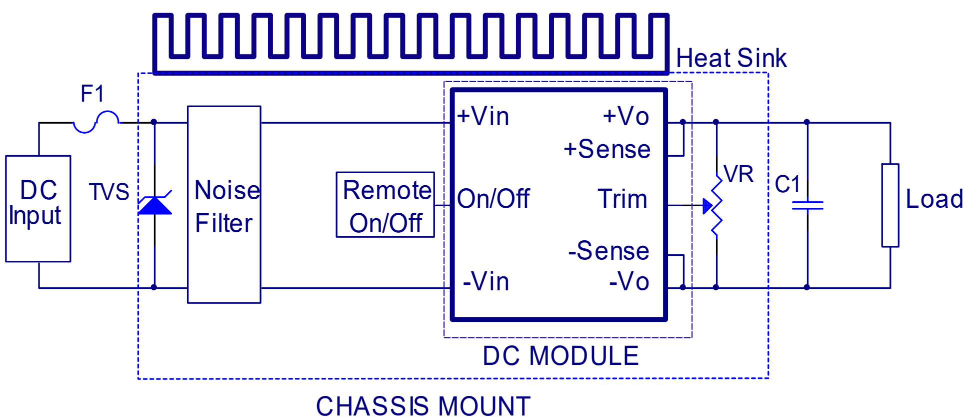
10.1 Input Fusing and Safety Considerations

The CFB750-300SXX-CMFD series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 10A time delay fuse for all models. Chassis mount module have a transient voltage suppressor diode (TVS) across the input terminal to protect the unit against surge or spike voltage and input reverse voltage (as shown).



10.2 EMC Considerations

(1) EMI Test standard: EN 55032 (EN 55022) Class A Conducted & Radiated Emission
Test Condition: Input Voltage: 300Vdc, Output Load: Full Load

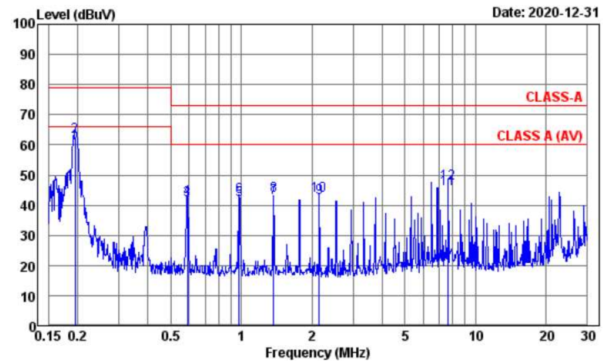


Connection circuit for EN55032 Class A EMI testing

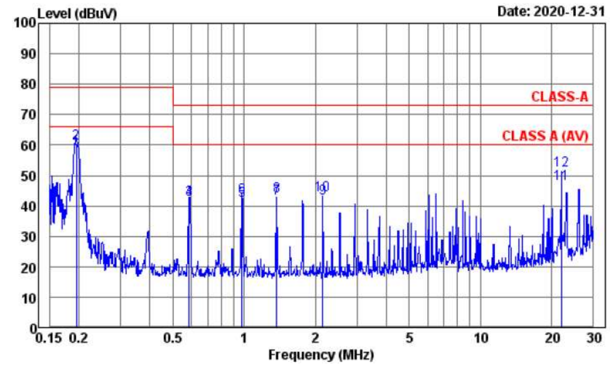
Input Conducted Emission:

CFB750-300S12(N)-CMFD

Line

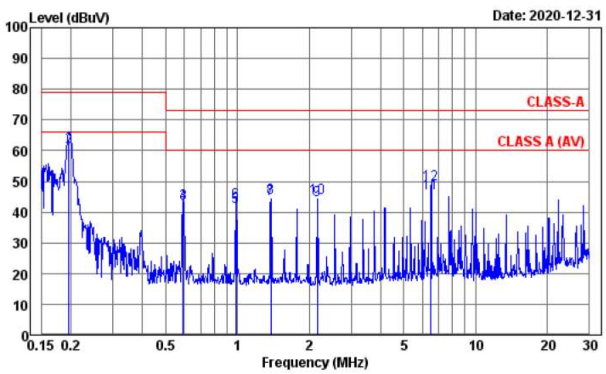


Neutral

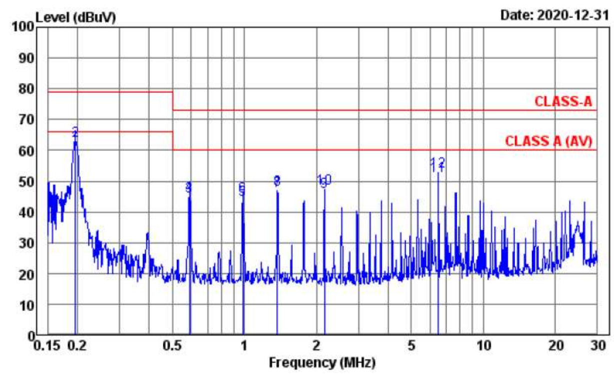


CFB750-300S15(N)-CMFD

Line

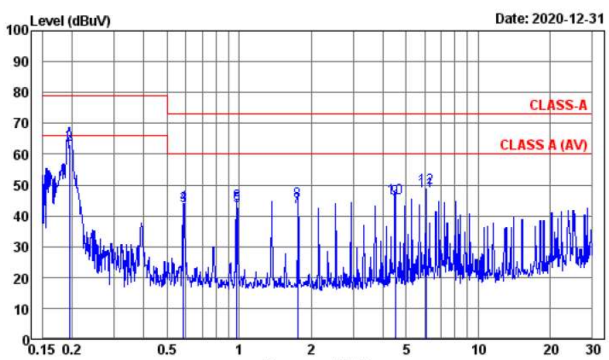


Neutral



CFB750-300S24(N)-CMFD

Line



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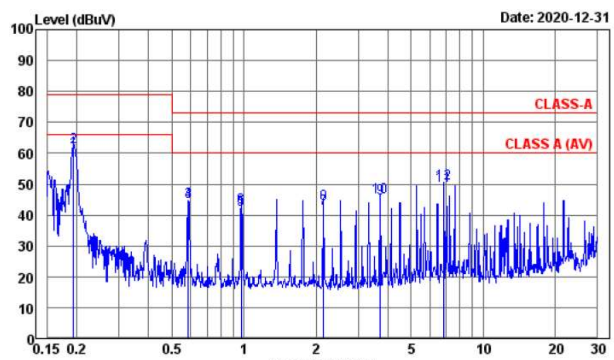
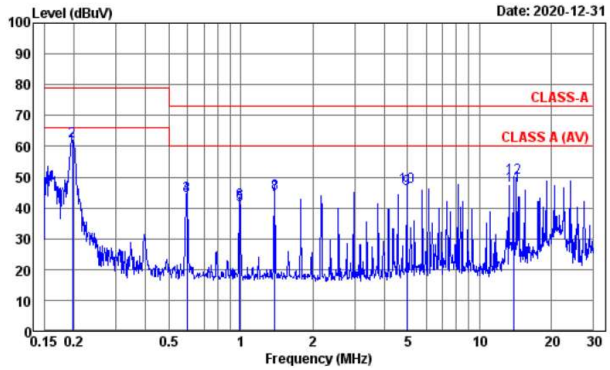


CFB750-300S28(N)-CMFD

Line



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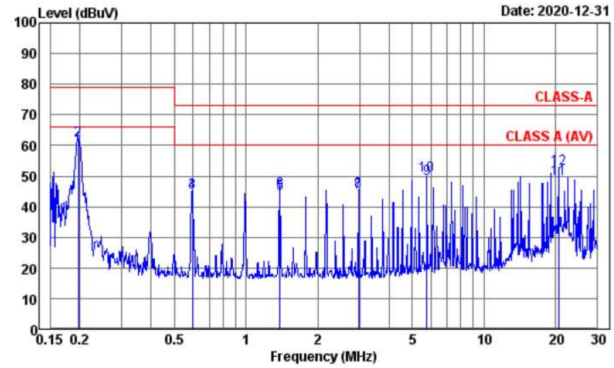
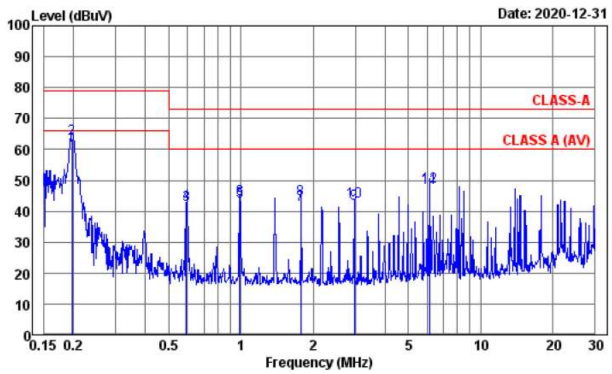


CFB750-300S36(N)-CMFD

Line



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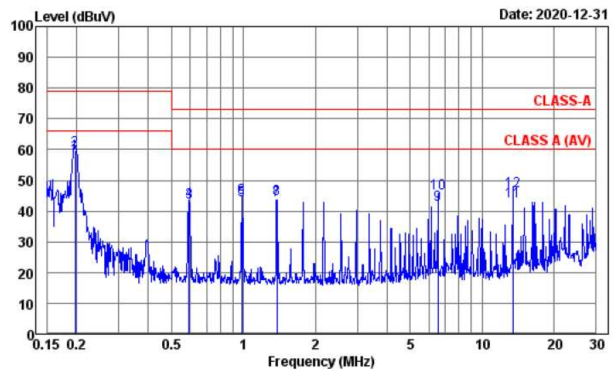
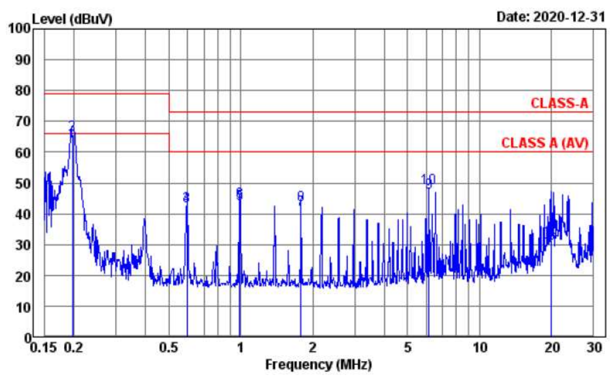


CFB750-300S48(N)-CMFD

Line



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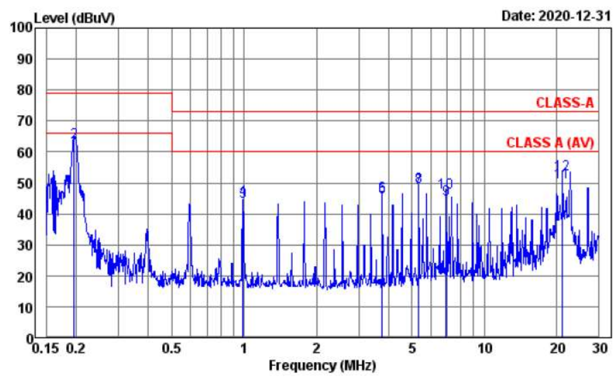
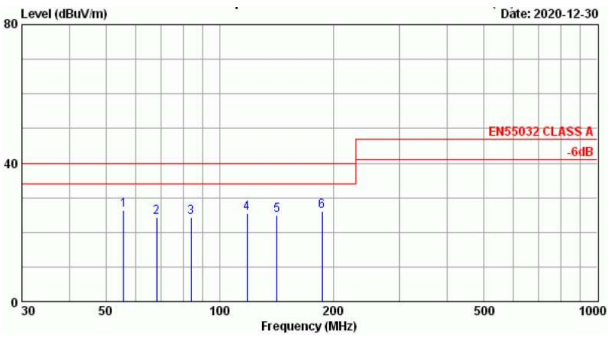


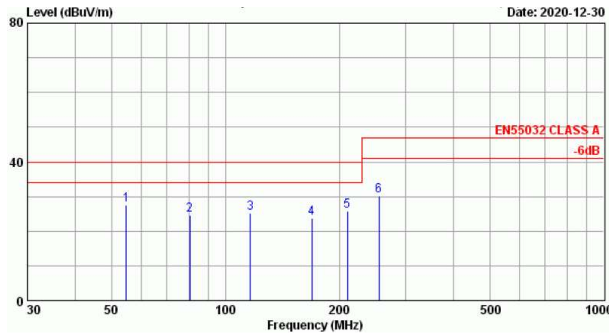
Radiated Emission:

CFB750-300S12(N)-CMFD

Vertical

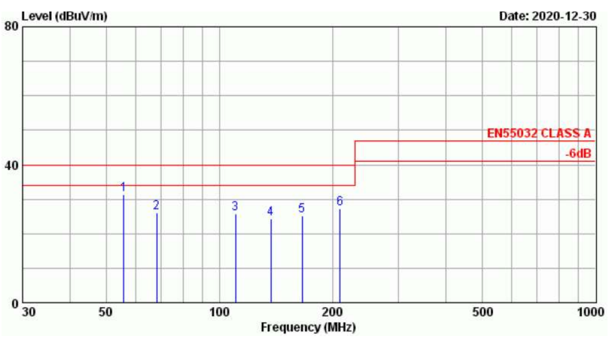


Horizontal



CFB750-300S15(N)-CMFD

Vertical



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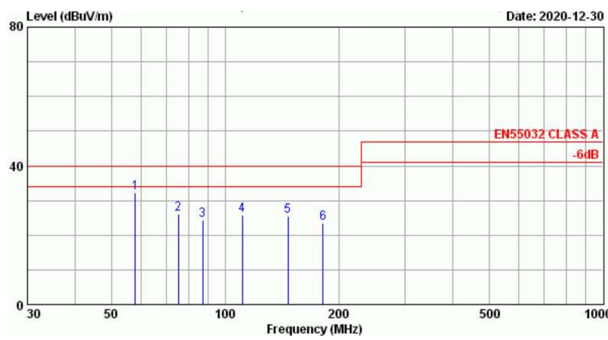
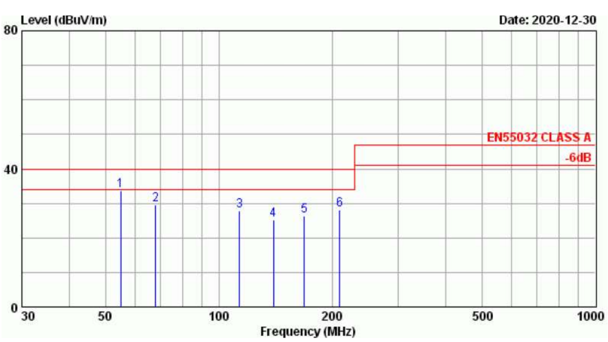
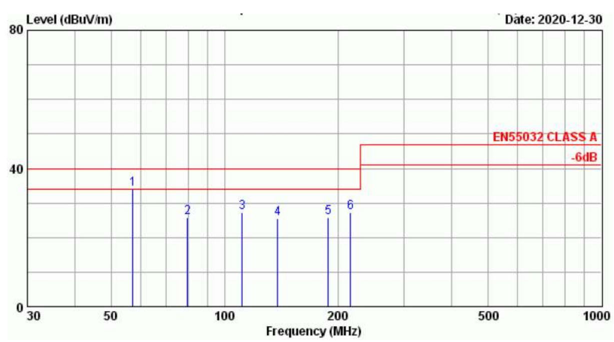


CFB750-300S24(N)-CMFD

Vertical

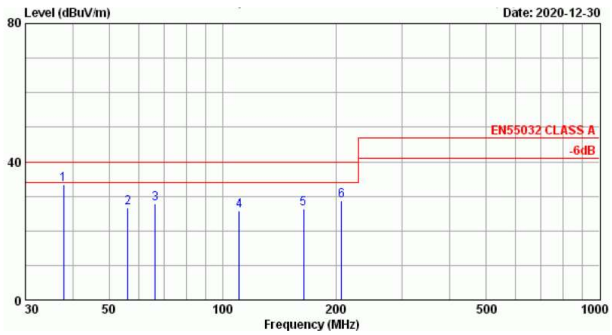


Horizontal



CFB750-300S28(N)-CMFD

Vertical



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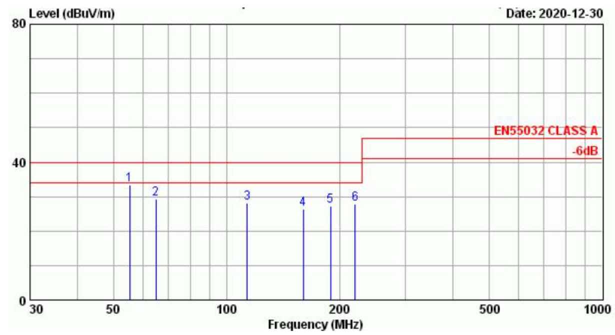
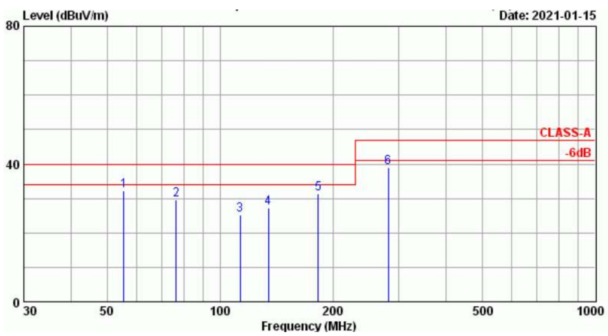
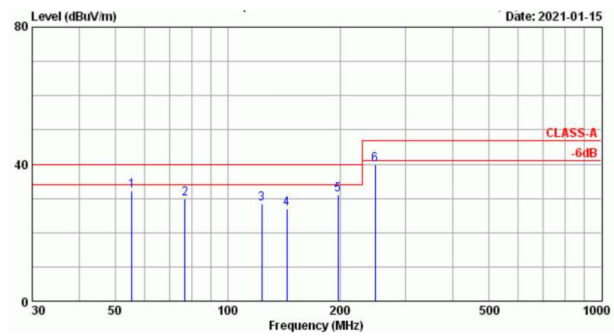


CFB750-300S36(N)-CMFD

Vertical

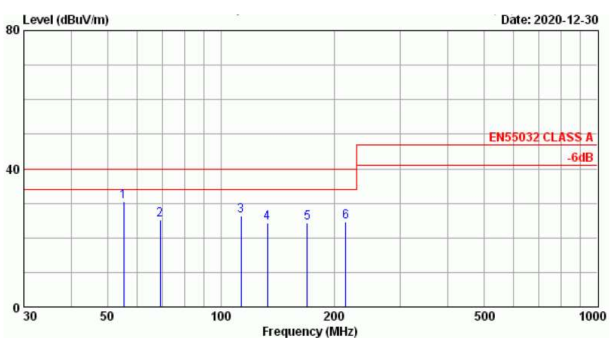


Horizontal

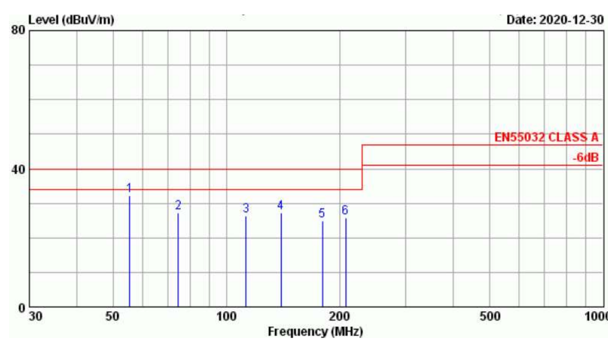


CFB750-300S48(N)-CMFD

Vertical



Horizontal



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